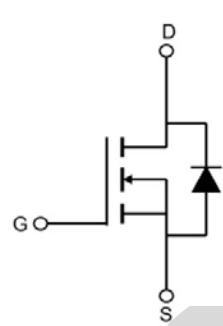


Trench N-channel Power MOSFET

TO-220
MSR012N13CT



To-220 Top View



Schematic Diagram

V_{DS}	130	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	8.5	mΩ
I_D	104	A

Features

- $V_{DS}=130\text{V}; I_D=104\text{A} @ V_{GS}=10\text{V}$;
- $R_{DS(ON)}<12.0\text{m}\Omega @ V_{GS}=10\text{V}$
- Special Designed for E-Bike Controller Application
- Ultra Low On-Resistance
- High UIS and UIS 100% Test

96V E-Bike controller applications

Hard Switched and High Frequency Circuits

Uninterruptible Power Supply

Maximum ratings, at $TA = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage	130	V
V_{GS}	Gate-Source voltage	± 25	V
dv/dt	Peak Diode Recovery Voltage	7	V/ns
I_D	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	A
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 1)	$T_C=25^\circ\text{C}$	A
EAS	Single Pulse Avalanche Energy (Note 2)	1600	mJ
P_D	Maximum power dissipation	$T_C=25^\circ\text{C}$	W
$T_{STG,TJ}$	Storage and Junction Temperature Range	-55 to 175	°C

Notes 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. EAS condition: $T_J=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $R_G=25\text{k}\Omega$

Thermal Characteristics

Symbol	Parameter	Value	Max	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	---	0.43	°C/W

Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
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Static Electrical Characteristics @T_j=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	130	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current(T _c =25)	V _{DS} =130V, V _{GS} =0V	--	--	1	μA
I _{DSS}	Zero Gate Voltage Drain Current(T _c =125)	V _{DS} =130V, V _{GS} =0V	--	--	10	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	3	--	5	V
R _{D(on)}	Drain-Source On-State Resistance ④	V _{GS} =10V, I _D =40A	--	8.5	12	mΩ

Dynamic Electrical Characteristics@T_j = 25°C (unless otherwise stated)

C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V , f=1MHz	--	9152	--	pF
C _{oss}	Output Capacitance		--	799	--	pF
C _{rss}	Reverse Transfer Capacitance		--	406	--	pF
g _{fs}	Forward Transconductance	V _{DS} = 10 V, ID = 15A	20	--	--	S
Q _g (10V)	Total Gate Charge	V _{DS} =50V, I _D =40A , V _{GS} =10V	--	274	--	nC
Q _{gs}	Gate-Source Charge		--	56	--	nC
Q _{gd}	Gate-Drain Charge		--	113	--	nC

Switching Characteristics

Td(on)	Turn-on Delay Time	V _{DD} =65V, ID=40A, R _L =15Ω, V _{GS} =10V, RG=2.5Ω	--	40	--	ns
Tr	Turn-on Rise Time		--	72	--	ns
Td(off)	Turn-Off Delay Time		--	103	--	ns
Tf	Turn-Off Fall Time		--	35	--	ns

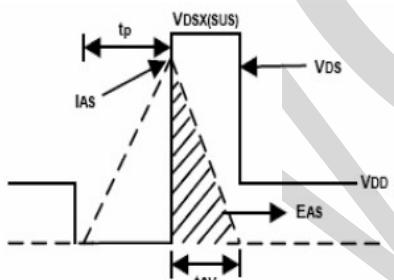
Source -Drain Diode Characteristics @T_j = 25°C (unless otherwise stated)

V _{SD}	Forward on voltage	I _{SD} =40A,V _{GS} =0V	--	0.85	0.99	V
T _{rr}	Reverse Recovery Time (Note1)	T _j =25, IF=40A di/dt=100A/μs	--	80	--	ns
Q _{rr}	Reverse Recovery Charge (Note1)		--	200	--	nC
I _{SD}	Source-Drain Current(Body Diode)			104		A
I _{SDM}	Pulsed Source-Drain Current(Body Diode)			416		A
t _{on}	Forward Turn-on Time	Intrinsic turn-on time is negligible(turn-on is dominated by LS+LD)				

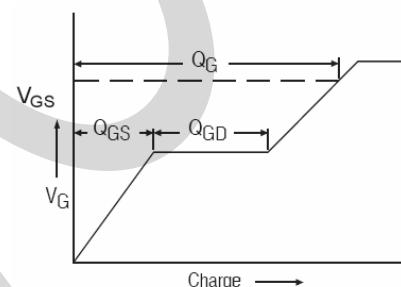
Notes1.Pulse Test: Pulse Width 300μs, Duty Cycle 1.5%, RG=25 , Starting T_j=25

Test Circuit

1) EASTest Circuits



2) Gate Charge Test Circuit:



3) SwitchTime Test Circuit :

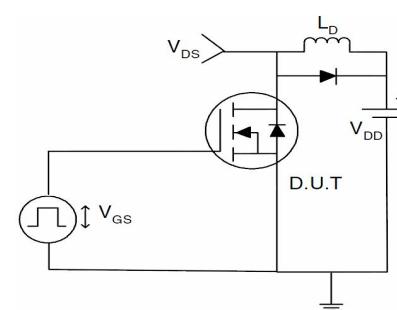
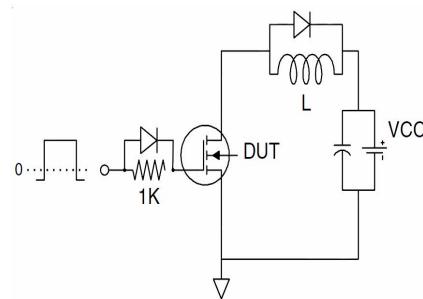
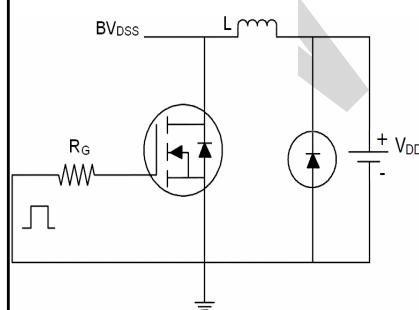
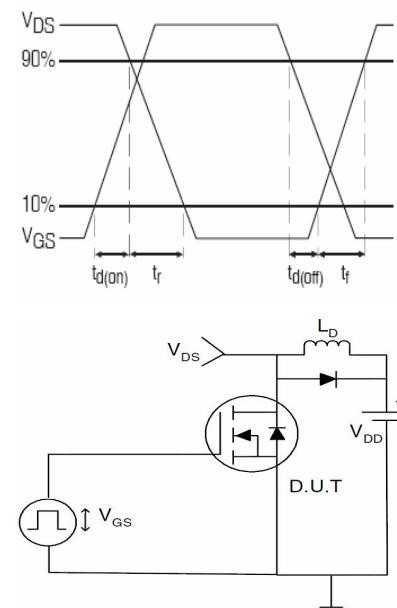


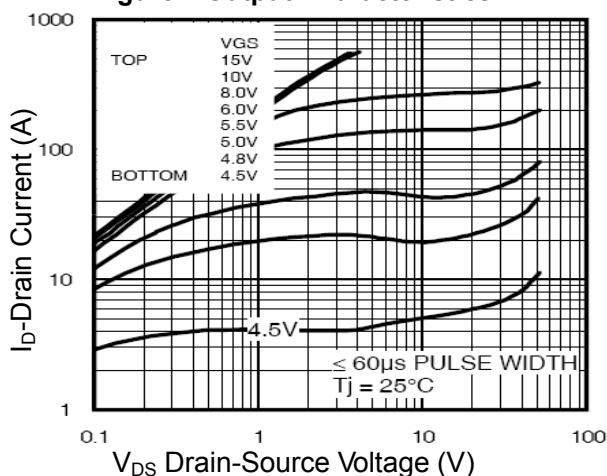
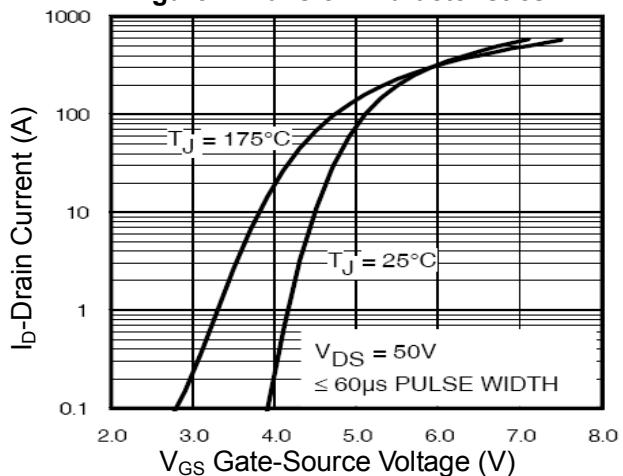
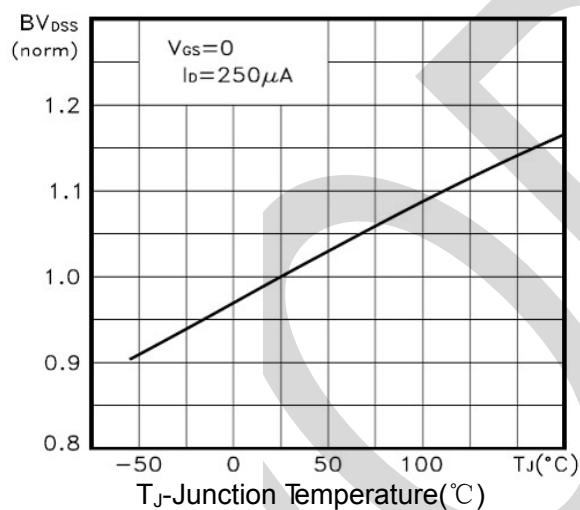
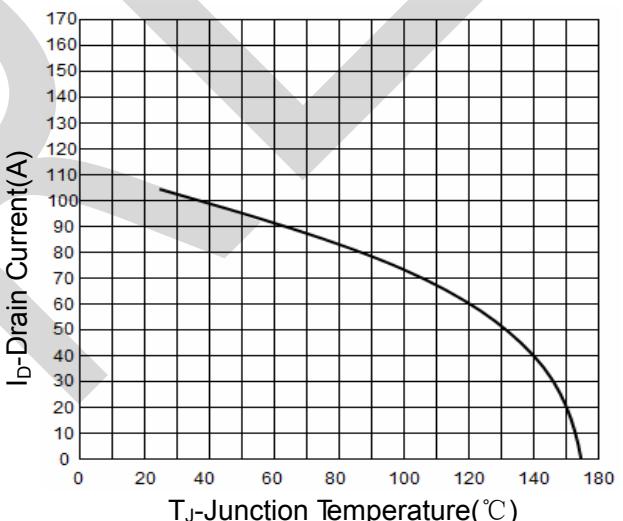
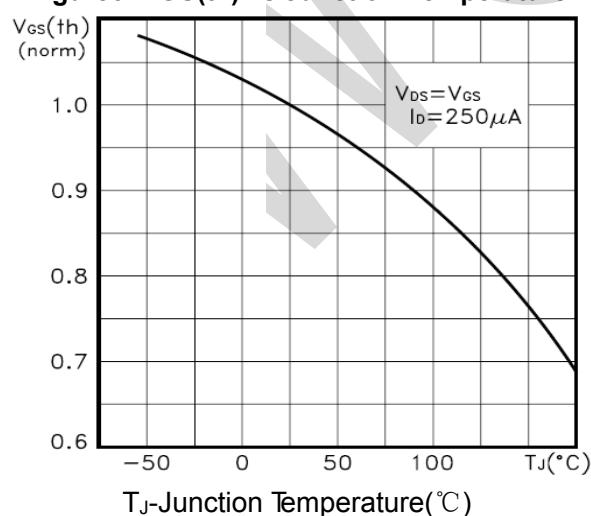
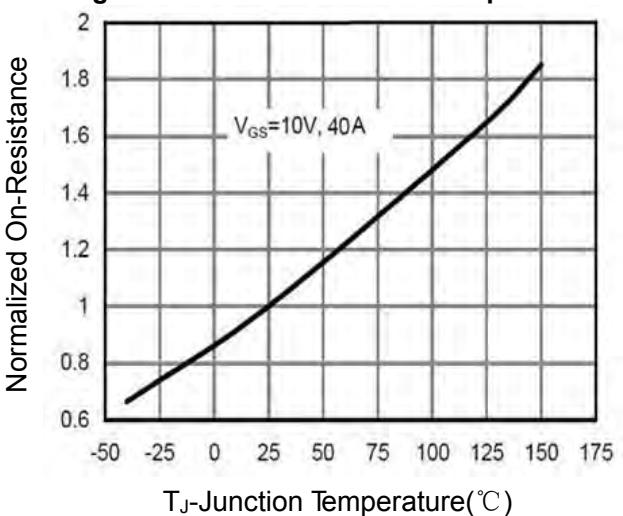
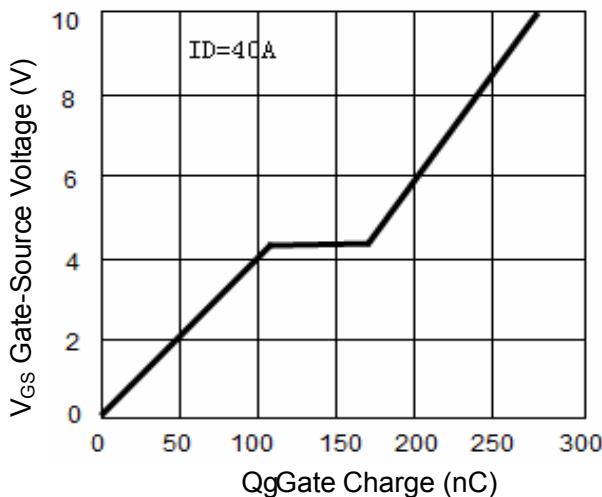
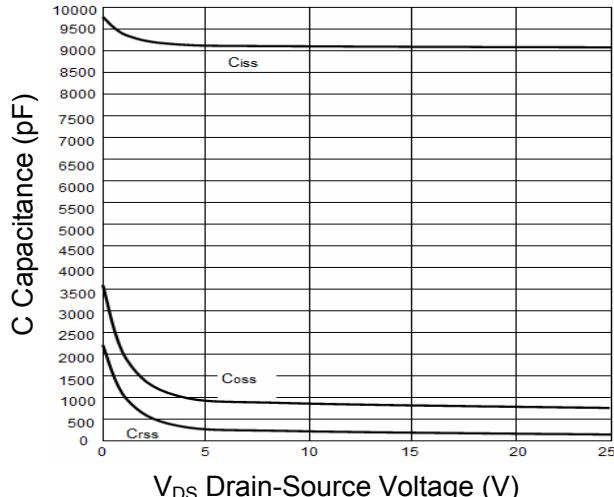
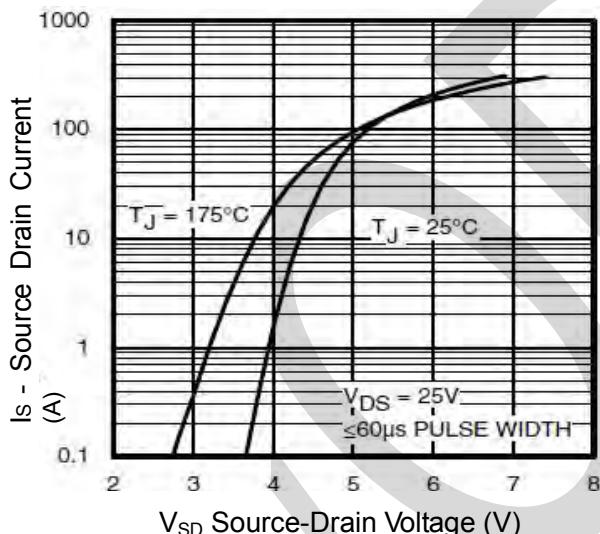
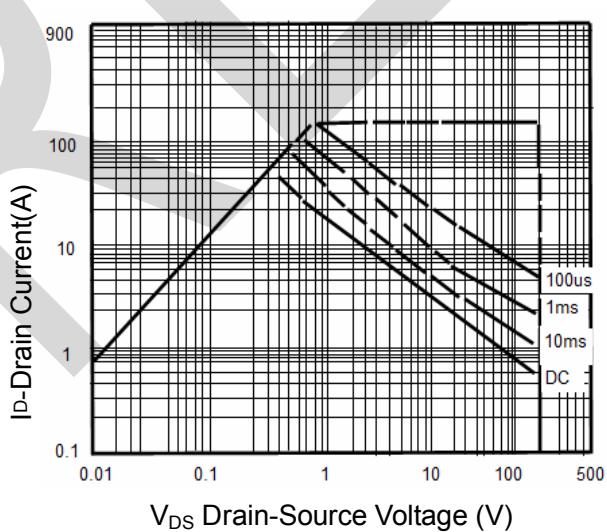
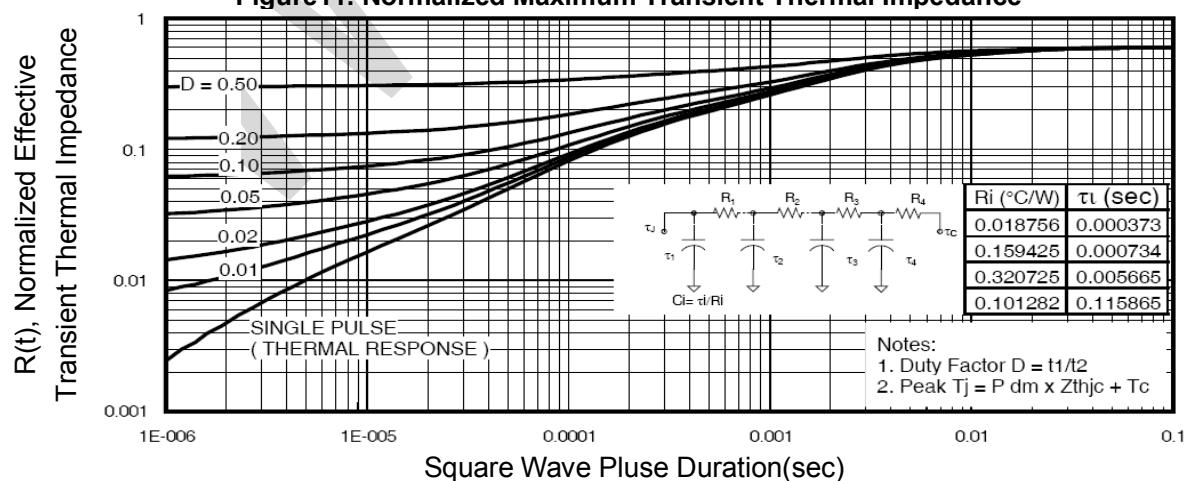
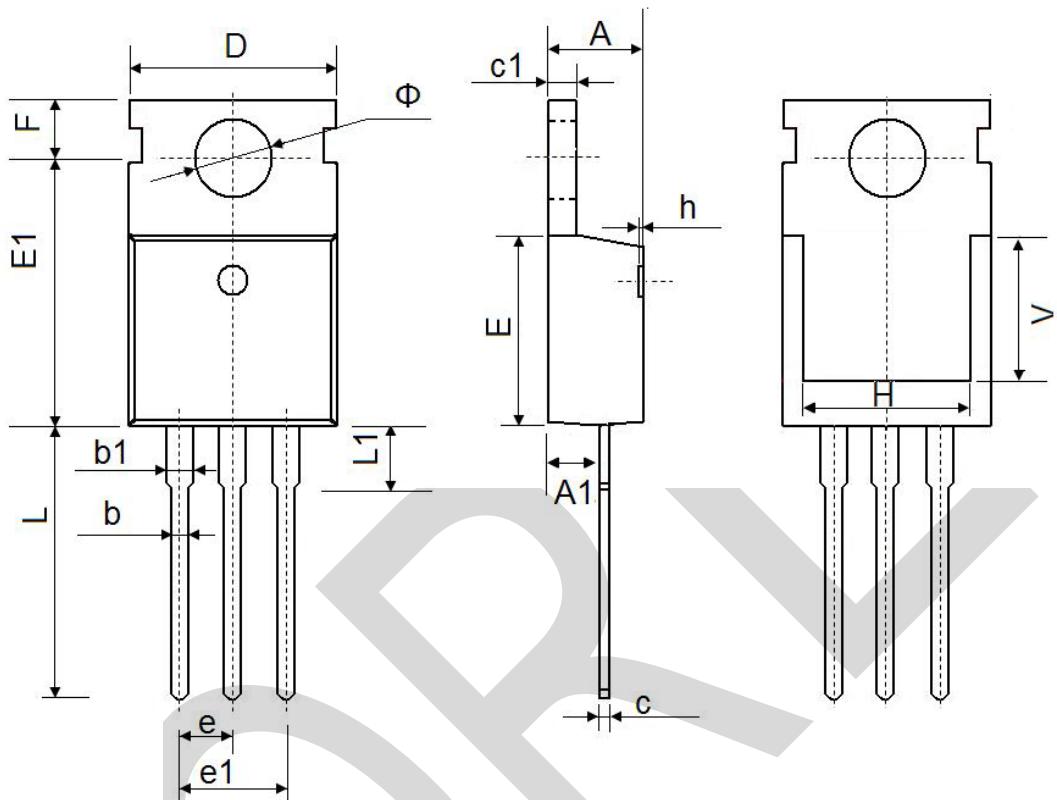
Figure1. Output Characteristics

Figure2. Transfer Characteristics

Figure3. BV_{DSS} vs Junction

Figure4. ID vs Junction Temperature

Figure5. V_{GS(th)} vs Junction Temperature

Figure6. Rdson Vs Junction Temperature


Figure7. Gate Charge

Figure8. Capacitance vs Vds

Figure9. Source- Drain Diode Forward

Figure10. Safe Operation Area

Figure11. Normalized Maximum Transient Thermal Impedance


TO-220 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.300	4.700	0.169	0.185
A1	2.200	2.600	0.087	0.102
b	0.700	0.950	0.028	0.037
b1	1.170	1.410	0.046	0.056
c	0.450	0.650	0.018	0.026
c1	1.200	1.400	0.047	0.055
D	9.600	10.400	0.378	0.409
E	8.8500	9.750	0.348	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.500	7.700	0.295	0.303
h	0.000	0.300	0.000	0.012
L	12.750	14.300	0.502	0.563
L1	2.850	3.950	0.112	0.156
V	6.80 REF.		0.267 REF.	
Φ	3.400	4.000	0.134	0.157