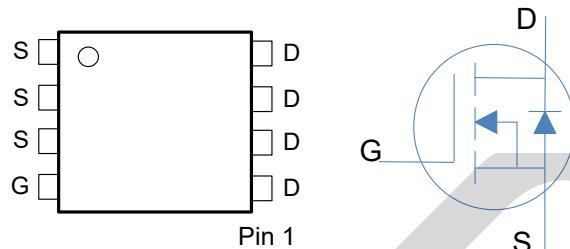
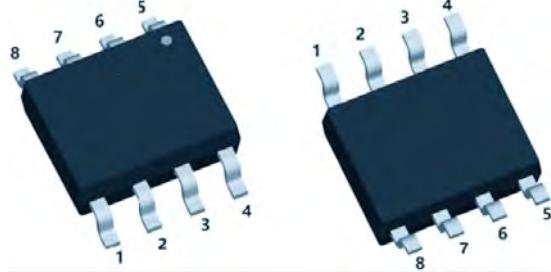


## SGT N-channel Power MOSFET

**MTR7R5N06P8 SOP8**



$V_{DS}$	60	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	6.5	mΩ
$I_D$	18	A

### Features

- 1、Low on – resistance
- 2、High power package (SOP8)
- 3、SGT N-channel Power MOSFET

### Applications

- 1、Load Switch for Portable Devices
- 2、DC/DC Converter

### Maximum ratings, at $T_A = 25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage	60	V
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$I_S$	Diode continuous forward current	$T_c=25^\circ\text{C}$	A
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_c=25^\circ\text{C}$	A
		$T_c=100^\circ\text{C}$	A
$I_{DM}$	Pulse drain current tested ①	$T_c=25^\circ\text{C}$	A
$I_{DSM}$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_A=25^\circ\text{C}$	A
		$T_A=70^\circ\text{C}$	A
$E_{AS}$	Avalanche energy, single pulsed ②	20	mJ
$P_D$	Maximum power dissipation	$T_c=25^\circ\text{C}$	W
		$T_c=100^\circ\text{C}$	W
$P_{DSM}$	Maximum power dissipation ③	$T_A=25^\circ\text{C}$	W
		$T_A=70^\circ\text{C}$	W
$T_{STG,TJ}$	Storage and Junction Temperature Range	-55 to 150	°C

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	3.4	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	30	°C/W

## Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
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### Static Electrical Characteristics @ T<sub>j</sub>=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60	68	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current (T <sub>j</sub> =125°C)	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.9	2.5	V
R <sub>D(on)</sub>	Drain-Source On-State Resistance ④	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	--	6.5	7.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A	--	10	12	mΩ

### Dynamic Electrical Characteristics @ T<sub>j</sub> = 25°C (unless otherwise stated)

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1MHz	995	1170	1345	pF
C <sub>oss</sub>	Output Capacitance		500	585	670	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	20	30	pF
R <sub>g</sub>	Gate Resistance	f=1MHz	--	1.3	--	Ω
Q <sub>g</sub> (10V)	Total Gate Charge	V <sub>DS</sub> =30V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V	--	22	--	nC
Q <sub>g</sub> (4.5V)	Total Gate Charge		--	12	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	4.2	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	5.5	--	nC

## Switching Characteristics

Td(on)	Turn-on Delay Time	$V_{DD}=30V$ , $I_D=30A$ , $R_G=3\Omega$ , $V_{GS}=10V$	--	7.4	--	ns
Tr	Turn-on Rise Time		--	43	--	ns
Td(off)	Turn-Off Delay Time		--	18	--	ns
Tf	Turn-Off Fall Time		--	6.2	--	ns

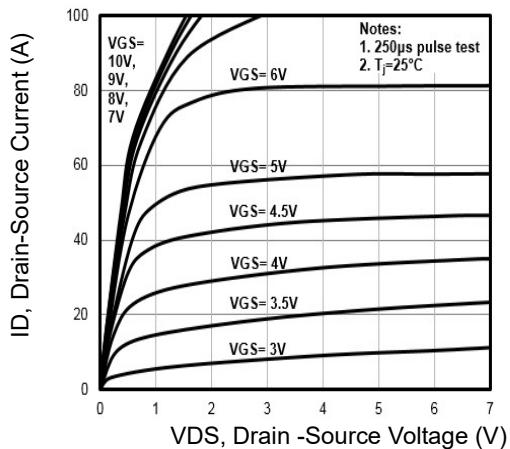
## Source- Drain Diode Characteristics@ $T_j = 25^\circ C$ (unless otherwise stated)

VSD	Forward on voltage	$I_{SD}=20A, V_{GS}=0V$	--	0.9	1.3	V
Trr	Reverse Recovery Time	$T_j=25^\circ C, I_{SD}=20A$ $, V_{GS}=0V$ $di/dt=100A/\mu s$	--	26	--	ns
Qrr	Reverse Recovery Charge		--	15	--	nC

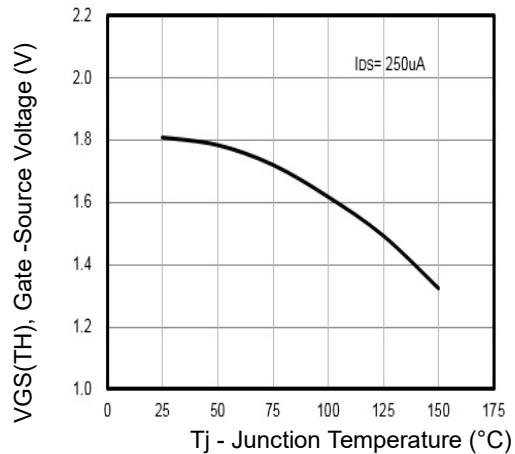
NOTE: ① Repetitive rating; pulse width limited by max junction temperature.

- ② Limited by  $T_{Jmax}$ , starting  $T_J = 25^\circ C$ ,  $L = 0.5mH$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 9A$ ,  $V_{GS} = 10V$ . Part not recommended for use above this value
- ③ The power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of  $150^\circ C$ .
- ④ Pulse width  $\leq 380\mu s$ ; duty cycle  $\leq 2\%$ .

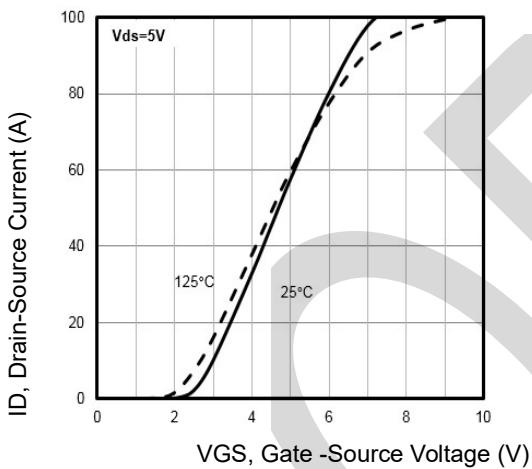
## Typical Characteristics



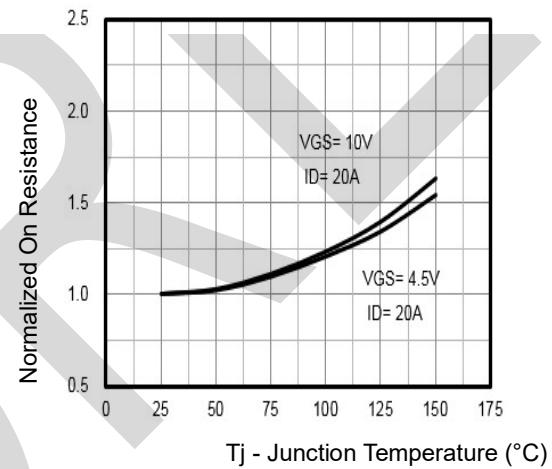
**Fig1.** Typical Output Characteristics



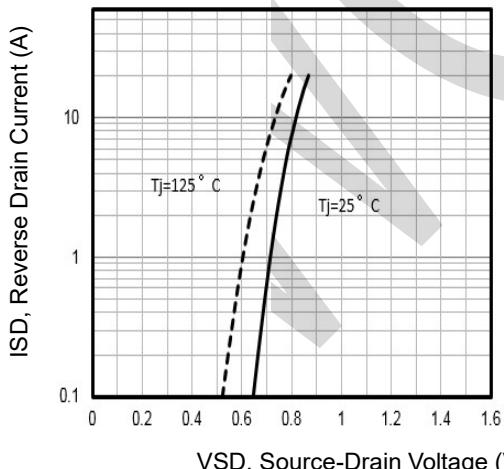
**Fig2.**  $V_{GS(TH)}$  Gate -Source Voltage Vs.  $T_j$



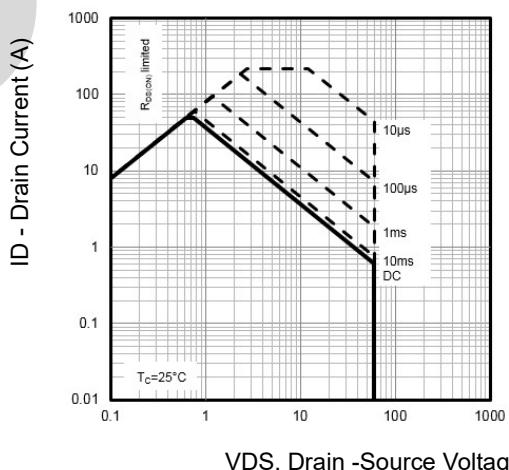
**Fig3.** Typical Transfer Characteristics



**Fig4.** Normalized On-Resistance Vs.  $T_j$



**Fig5.** Typical Source-Drain Diode Forward Voltage



**Fig6.** Maximum Safe Operating Area

## Typical Characteristics

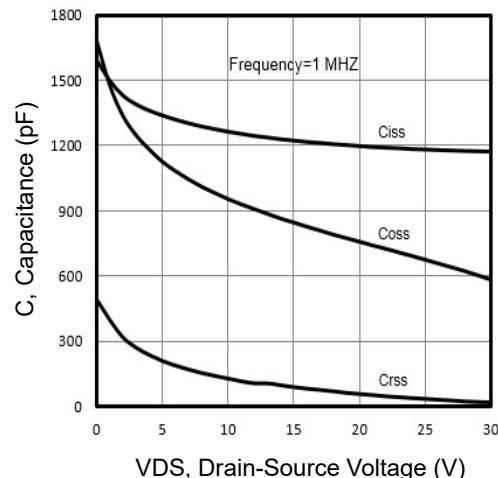


Fig7. Typical Capacitance Vs. Drain-Source Voltage

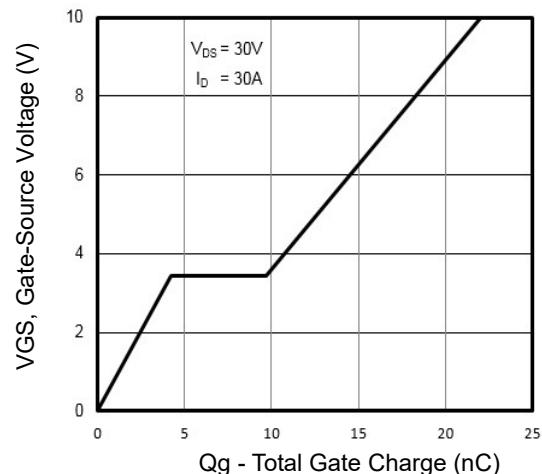


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

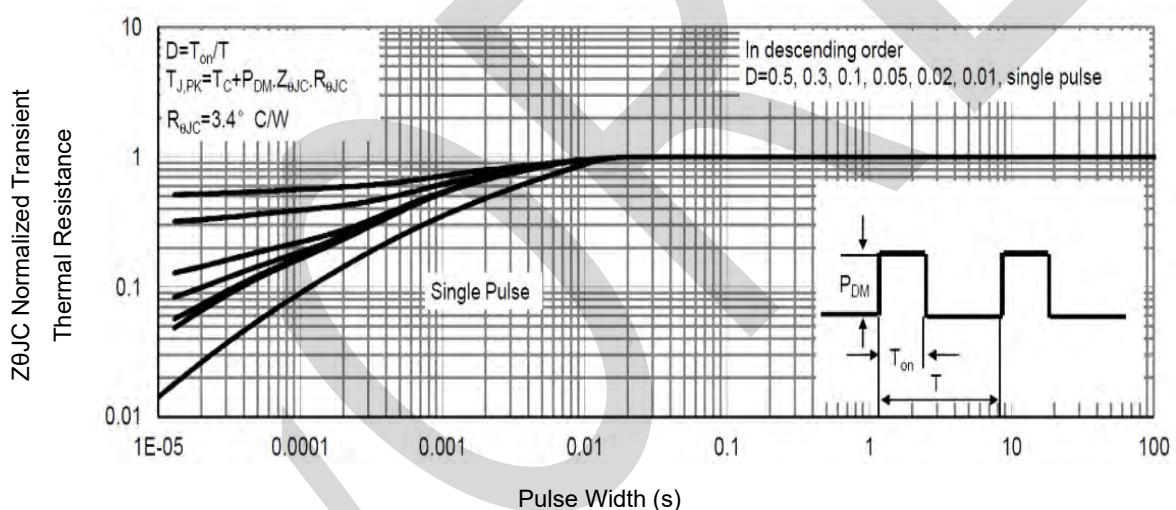


Fig9. Normalized Maximum Transient Thermal Impedance

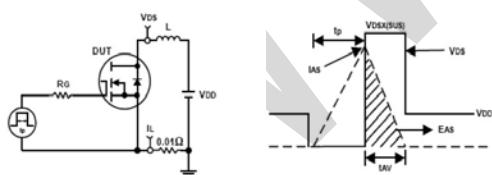


Fig10. Unclamped Inductive Test Circuit and waveforms

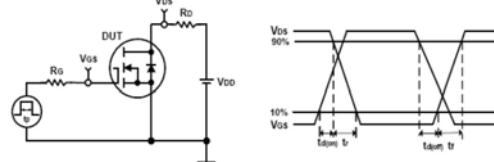
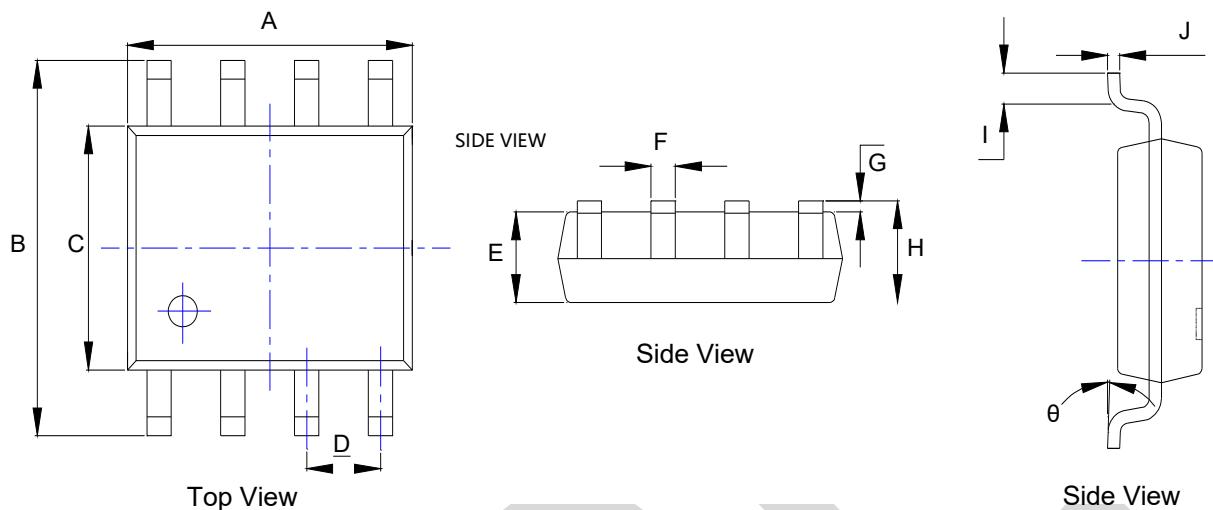


Fig11. Switching Time Test Circuit and waveforms

## PACKAGE OUTLINE DIMENSIONS



## SOP8 mechanical data

UNIT		A	B	C	D	E	F	G	H	I	J	$\theta$
mm	min	4.70	5.70	3.80	1.27 TYP	1.25	0.33	0.10	1.30	0.40	0.17	0°
	max	5.10	6.30	4.00		1.55	0.51	0.25	1.80	1.27	0.25	8°
mil	min	185.0	224.4	149.6	50.0 TYP	49.2	13.0	3.9	51.1	15.7	6.7	0°
	max	200.8	248.0	157.5		61.1	20.1	9.9	70.9	50.0	9.8	8°

## SOP8 Suggested Pad Layout

