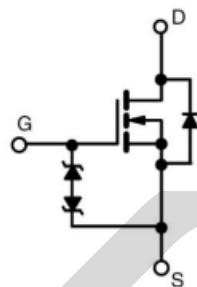


N-Channel Super Junction MOSFET

MCR65F105PT

TO-247

**FRD MOSFET
ESD protect**



V_{DS}	650	V
$R_{DS(on),TYP}$	95	mΩ
I_D	30	A

Features

- 1.Low on – resistance
- 2.Reduced Switching & Conduction Losses
- 3.Fast Recovery Body-Diode
- 4.Package TO-247

Applications

- 1.PC power
- 2.Telcom power
- 3.Server power
- 4.EV Charger
- 5.Motor driver

Maximum ratings, at $T_A = 25^\circ\text{C}$, unless otherwise specified

Absolute Maximum Ratings

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	650	V
Drain Current –continuous @ 25°C	I_D	30	A
Pulsed Drain Current ¹	I_{DM}	105	A
Gate-Source Voltage	V_{GS}	± 30	V
Single Pulse Avalanche ²	E_{AS}	1250	mJ
Operating Junction	T_j	-55 to 150	°C
Storage temperature	T_{stg}	-55 to 150	°C

Note:

1. Pulse width limited by maximum junction temperature.
2. $I_D = 6.2\text{A}$, $V_{DD} = 50\text{V}$, starting $T_J = 25^\circ\text{C}$.

Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
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Electrical Characteristics ($T_C = 25^\circ\text{C}$ unless otherwise noted)

$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=1\text{mA}$	650	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=650\text{V}, V_{\text{GS}}=0\text{V}$	--	--	20	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 30\text{V}$	--	--	± 100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	3	4	5	V
$R_{\text{DS(on)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=15\text{A}$	--	95	105	$\text{m}\Omega$

Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)

C_{iss}	Input Capacitance	$V_{\text{DS}}=380\text{V}, V_{\text{GS}}=0\text{V}, f=100\text{KHz}$	--	3100	--	pF
C_{oss}	Output Capacitance		--	67	--	pF
C_{rss}	Reverse Transfer Capacitance		--	4.4	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=380\text{V}, I_{\text{D}}=16\text{A}, V_{\text{GS}}=10\text{V}$	--	68	--	nC
Q_{gs}	Gate-Source Charge		--	14	--	nC
Q_{gd}	Gate-Drain Charge		--	25	--	nC

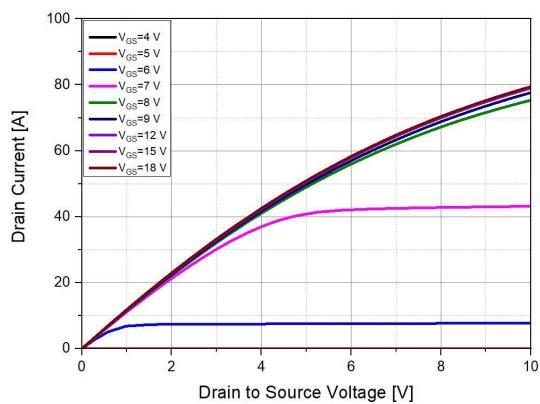
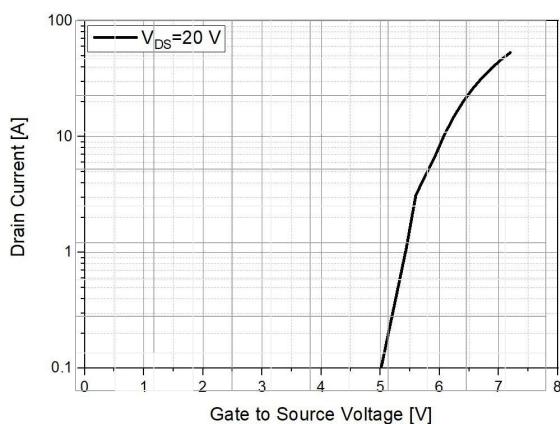
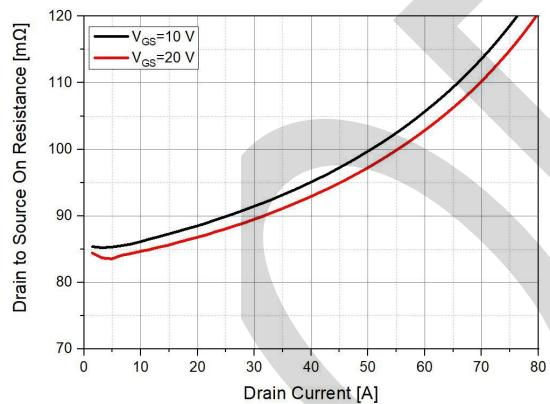
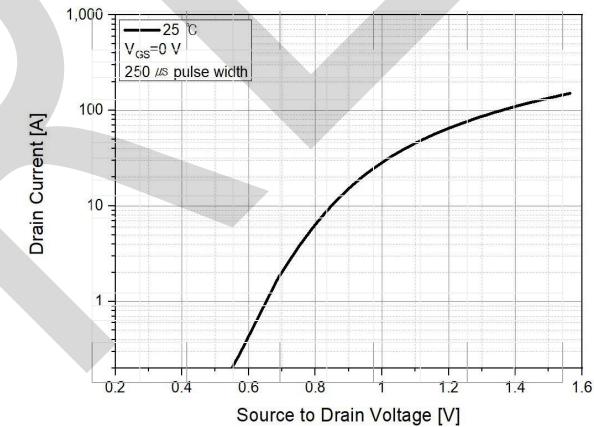
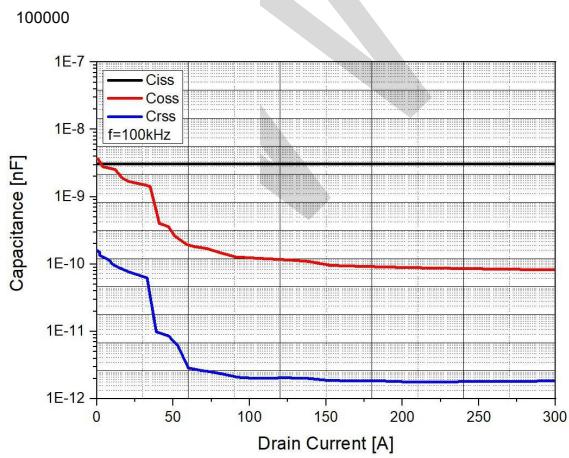
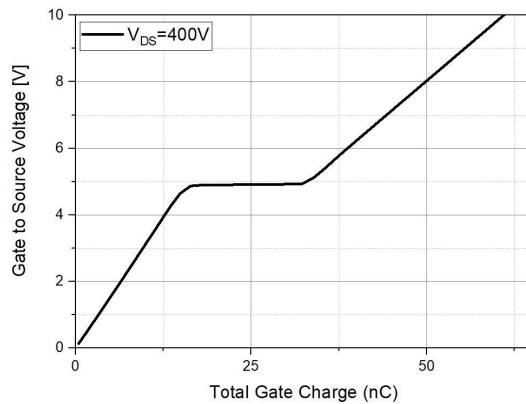
Switching Characteristics

$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DS}}=380\text{V}, I_{\text{D}}=15\text{A}, R_{\text{G}}=4.7\Omega, V_{\text{GS}}=10\text{V}$	--	10	--	ns
T_r	Turn-on Rise Time		--	5	--	ns
$T_{\text{d(off)}}$	Turn-Off Delay Time		--	80	--	ns
T_f	Turn-Off Fall Time		--	14	--	ns

Source- Drain Diode Characteristics@ $T_j = 25^\circ\text{C}$ (unless otherwise stated)

I_s	Continuous Current		--	--	30	A
I_{sm}	Maximum pulsed drain to source diode forward current		--	--	105	A
V_{SD}	Forward Voltage	$V_{GS} = 0\text{V}, I_s = 15\text{ A}$	--	--	0.9	V
T_{rr}	Reverse Recovery Time		--	185	--	ns
Q_{rr}	Reverse Recovery Charge	$V_R = 380\text{ V}, I_{SD} = 15\text{A}, dI_F/dt = 100\text{ A}/\mu\text{s}$	--	1.4	--	μC
I_{rm}	Reverse recovery current		--	16	--	A
T_{on}	Forward Turn-on Time	Intrinsic turn-on time is negligible(turn-on is dominated by L_S+L_D)				

Typical Performance Characteristics

Figure 1 . On- State Characteristics

Figure 2 . Transfer Characteristics

Figure 3 . On- Resistance Characteristics vs Drain Current and Gate Voltage

Figure 4 . On- State Characteristics Current vs Doide forward Voltage

Figure 5 . Capacitance Characteristics

Figure 6 . Gate Charge Characteristics


Typical Performance Characteristics

Figure 7 . Drain to Source Resistance vs Gate to Source voltage

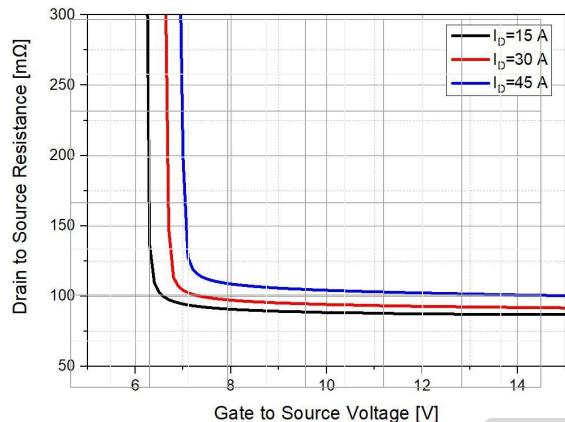


Figure 8 . Drain to Source Resistanc vs Gate to Source voltage

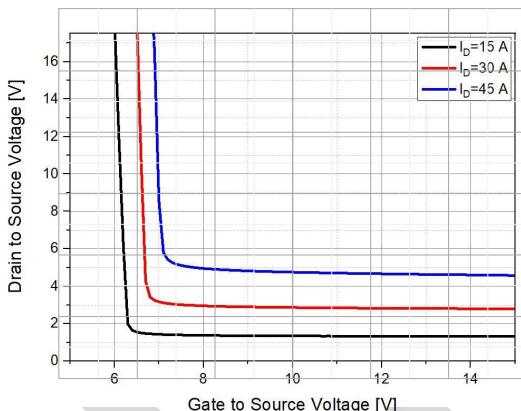
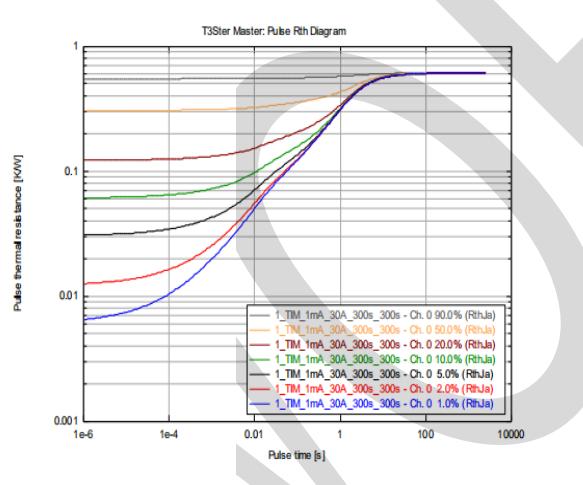


Figure 9 . Transient thermal response



PACKAGE OUTLINE DIMENSIONS

TO-247

