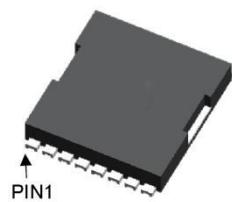


## SGT N-channel Power MOSFET

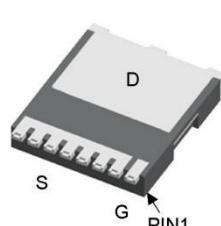
**MTR1R2N04TL**

**TOLL**

TOLL Top View



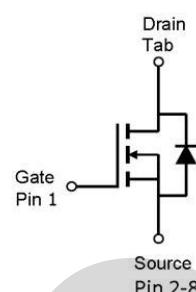
TOLL Bottom View



$V_{DS}$	40	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	0.9	$\text{m}\Omega$
$I_D$	250	A

### Features

- 1、Low on – resistance
- 2、Package TOLL
- 3、SGT N-channel Power MOSFET



### Applications

- 1、Load Switch for Portable Devices
- 2、DC/DC Converter

**Maximum ratings, at  $T_A = 25^\circ\text{C}$ , unless otherwise specified**

Symbol	Parameter		Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage		40	V
$V_{GS}$	Gate-Source voltage		$\pm 20$	V
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_c=25^\circ\text{C}$ (Silicon limit)	250	A
		$T_c=100^\circ\text{C}$ (Silicon limit)	175	A
$I_{DM}$	Pulse drain current tested ①	$T_c=25^\circ\text{C}$	1000	A
$E_{AS}$	Avalanche energy, single pulsed ②		1056	$\text{mJ}$
$P_D$	Maximum power dissipation	$T_c=25^\circ\text{C}$	300	W
$T_{STG,TJ}$	Storage and Junction Temperature Range		-55 to +150	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	0.5	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	48	°C/W

## Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
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### Static Electrical Characteristics @ T<sub>j</sub>=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	47	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.1	1.6	2.4	V
R <sub>D(on)</sub>	Drain-Source On-State Resistance ④	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	--	0.9	1.2	mΩ

### Dynamic Electrical Characteristics @ T<sub>j</sub> = 25°C (unless otherwise stated)

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V , f=1MHz	--	8300	--	pF
C <sub>oss</sub>	Output Capacitance		--	1510	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	130	--	pF
R <sub>g</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	--	2.3	--	Ω
Q <sub>g</sub> (10V)	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =32V, I <sub>D</sub> =25A	--	92	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	44	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	22	--	nC

## Switching Characteristics

Td(on)	Turn-on Delay Time	VGS=10V, VDS=20V, RL=2.0Ω, Tj=25°C	--	22	--	ns
Tr	Turn-on Rise Time		--	7	--	ns
Td(off)	Turn-Off Delay Time		--	80	--	ns
Tf	Turn-Off Fall Time		--	26	--	ns

## Source- Drain Diode Characteristics@ T<sub>j</sub> = 25°C (unless otherwise stated)

VSD	Forward on voltage	I <sub>SD</sub> =50A, V <sub>GS</sub> =0V	--	0.75	1.2	V
Trr	Reverse Recovery Time	I <sub>F</sub> =30A, di/dt=500A/μs	--	100	--	ns
Qrr	Reverse Recovery Charge	I <sub>F</sub> =30A, di/dt=500A/μs	--	163	--	nC

NOTE: ① Repetitive rating; pulse width limited by max junction temperature.

- ② Limited by T<sub>Jmax</sub>, starting T<sub>J</sub> = 25°C, L = 0.5mH, R<sub>G</sub> = 25Ω. Part not recommended for use above this value
- ③ The power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C.
- ④ Pulse width ≤ 380μs; duty cycle≤ 2%.

## Typical Performance Characteristics

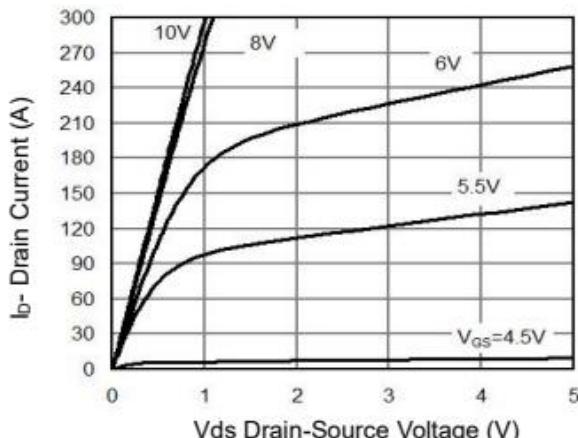


Figure 1 Output Characteristics

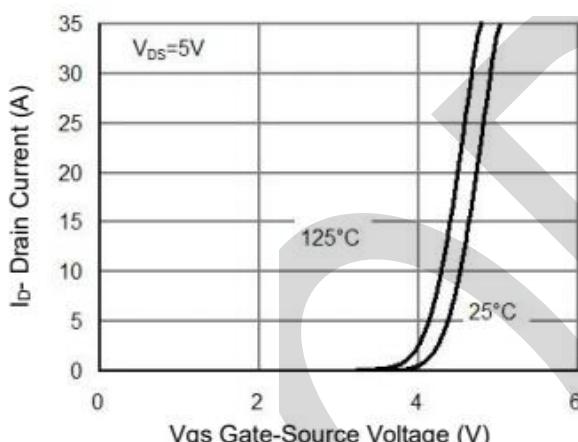


Figure 2 Transfer Characteristics

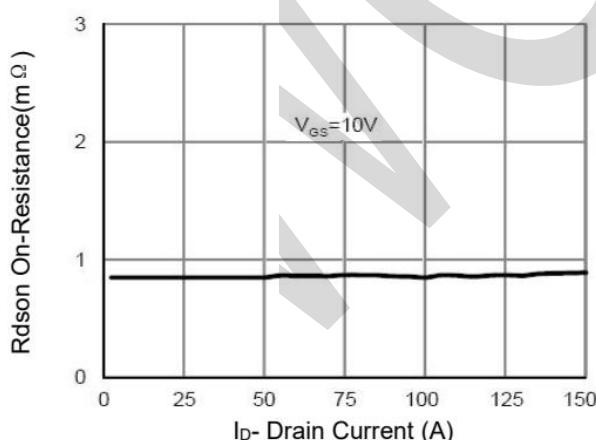


Figure 3 Rdson- Drain Current

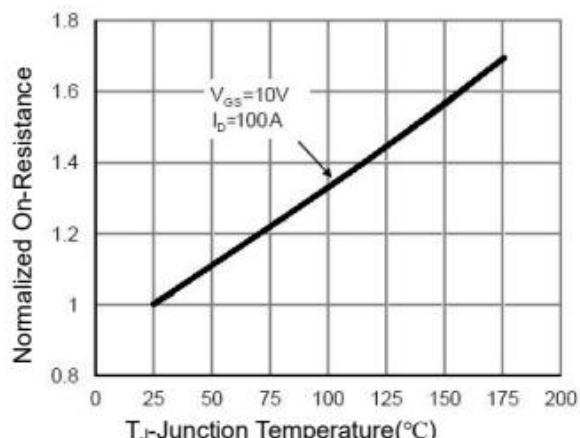


Figure 4 Rdson-Junction Temperature

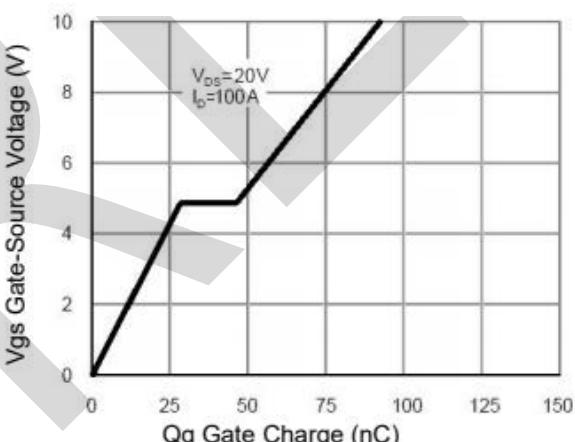


Figure 5 Gate Charge

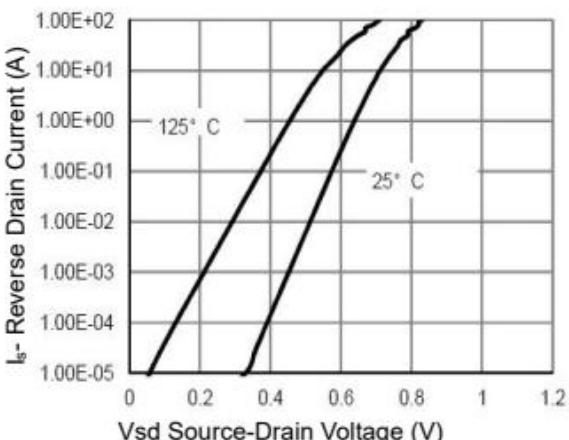
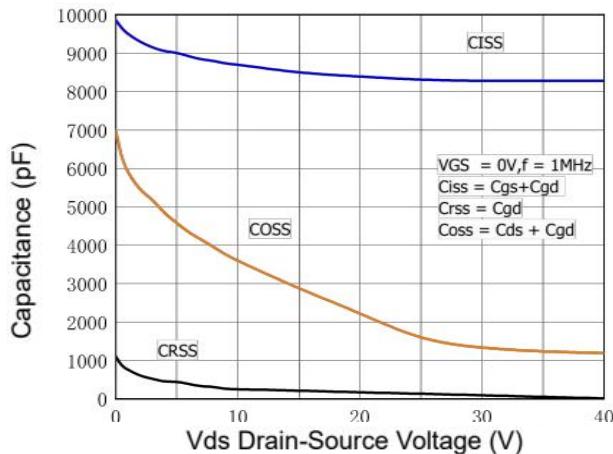
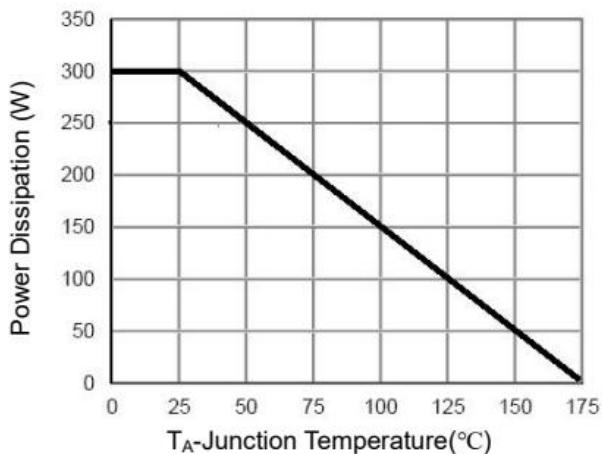


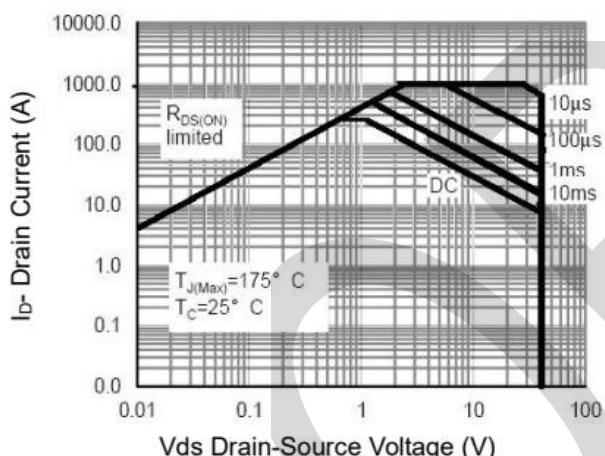
Figure 6 Source- Drain Diode Forward



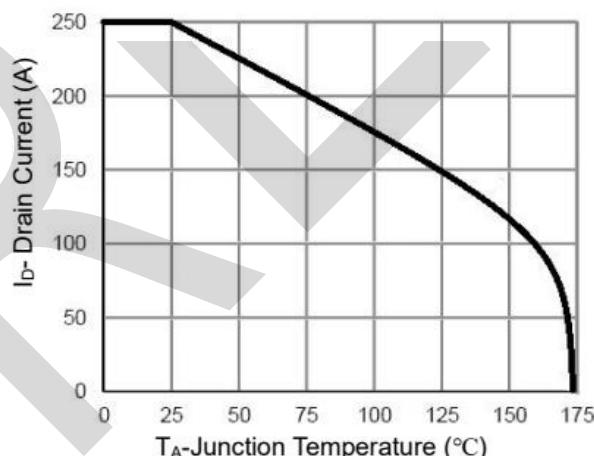
**Figure 7 Capacitance vs Vds**



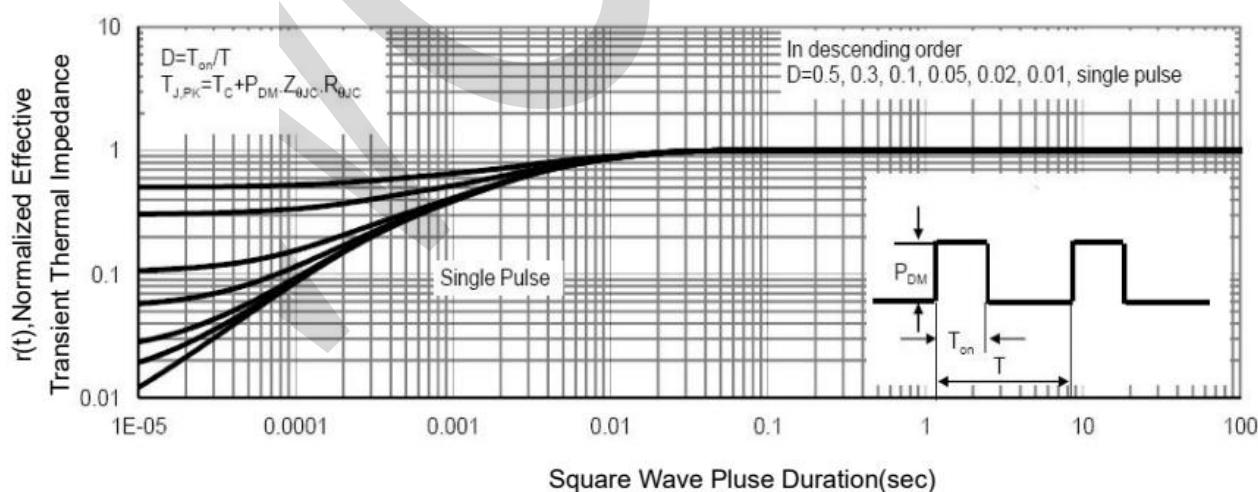
**Figure 9 Power De-rating**



**Figure 8 Safe Operation Area** (Note3)



**Figure 10 Current De-rating**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

## PACKAGE OUTLINE DIMENSIONS

**TOLL:(MM)**

