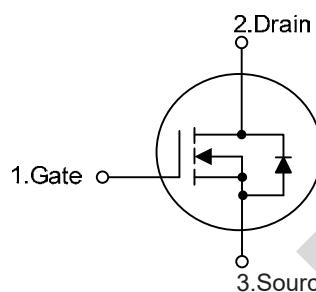


SGT N-channel Power MOSFET

MTR005N08CTB

TO-263



V_{DS}	85	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	4.2	mΩ
I_D	140	A

Features

- 1、Low on – resistance
- 2、Package TO-263
- 3、SGT N-channel Power MOSFET

Applications

- 1、Load Switch for Portable Devices
- 2、DC/DC Converter

Maximum ratings, at $T_A = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V(BR)DSS$	Drain-Source breakdown voltage	85	V
V_{GS}	Gate-Source voltage	± 20	V
I_D	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C=25^\circ\text{C}$	A
I_{DM}	Pulse drain current tested(Note 1)	$T_C=25^\circ\text{C}$	A
EAS	Avalanche energy, single pulsed(Note 2)	156	mJ
P_D	Maximum power dissipation	$T_C=25^\circ\text{C}$	174
	Derating Factor		0.5
$T_{STG,TJ}$	Storage and Junction Temperature Range	-55 to 150	°C

Notes:

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2.EAS condition: $T_J=25^\circ\text{C}, L=10\text{mH}$.

Thermal Characteristics

Symbol	Parameter	Typical	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	0.72	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
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Static Electrical Characteristics @ T_j=25°C (unless otherwise stated)

V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	85	95	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =85V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	2.0	3.0	4.0	V
R _{D(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =50A	--	4.2	5.0	mΩ

Dynamic Electrical Characteristics @ T_j = 25°C (unless otherwise stated)

C _{iss}	Input Capacitance	V _{DS} =40V, V _{GS} =0V, f=1MHz	--	4021	--	pF
C _{oss}	Output Capacitance		--	637	--	pF
C _{rss}	Reverse Transfer Capacitance		--	17	--	pF
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =40A	--	60	--	S
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V	--	1.8	--	Ω
Q _g (10V)	Total Gate Charge	V _{DS} =40V, I _D =50A, V _{GS} =10V	--	80	--	nC
Q _{gs}	Gate-Source Charge		--	23	--	nC
Q _{gd}	Gate-Drain Charge		--	24	--	nC

Switching Characteristics

Td(on)	Turn-on Delay Time	V _{DSS} =40V, I _D =50A, R _G =3Ω, V _{GS} =10V, R _L =1.5Ω	--	22	--	ns
Tr	Turn-on Rise Time		--	42	--	ns
Td(off)	Turn-Off Delay Time		--	48	--	ns
Tf	Turn-Off Fall Time		--	25	--	ns

Source- Drain Diode Characteristics@ T_j = 25°C (unless otherwise stated)

I _{SD}	Source-drain Current(Body Diode)		--	--	140	A
I _{SDM}	Pulsed Source-Drain Current (Body Diode)		--	--	520	A
V _{SD}	Forward on voltage (Note1)	I _{SD} =50A, V _{GS} =0V	--	--	1.2	V
T _{rr}	Reverse Recovery Time (Note1)	I _F =20A, di/dt=100A/μs	--	60	--	ns
Q _{rr}	Reverse Recovery Charge (Note1)		--	136	--	nC
T _{on}	Forward Turn-on Time	Intrinsic turn-on time is negligible(turn-on is dominated by L _S +L _D)				

Notes:

- 1.Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

Typical Characteristics

Figure 1. Typical Output Characteristics

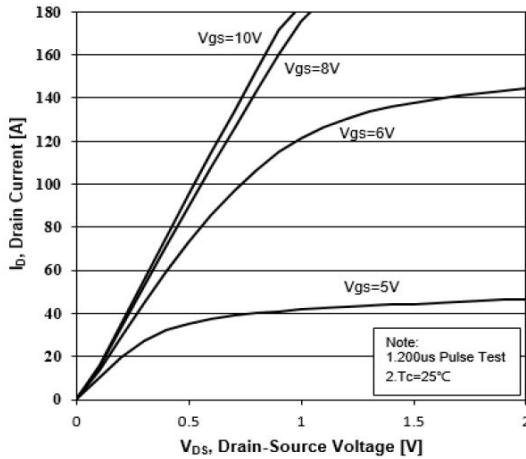


Figure 2. Typical Transfer Characteristics

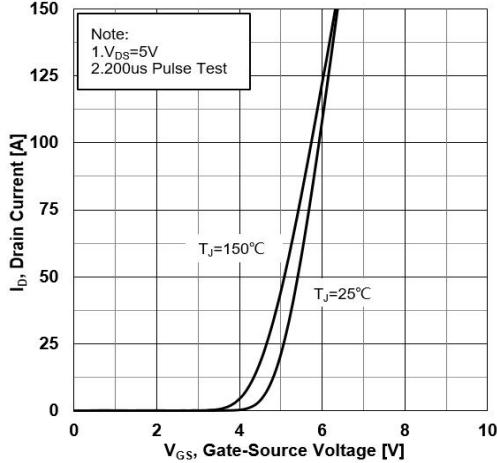


Figure 3. $R_{DS(ON)}$ vs. Drain Current

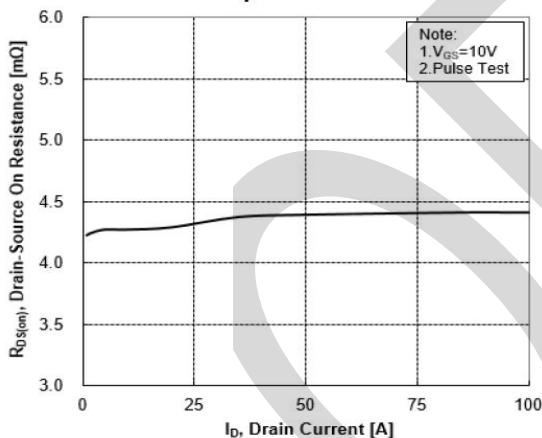


Figure 4. Body Diode Characteristics

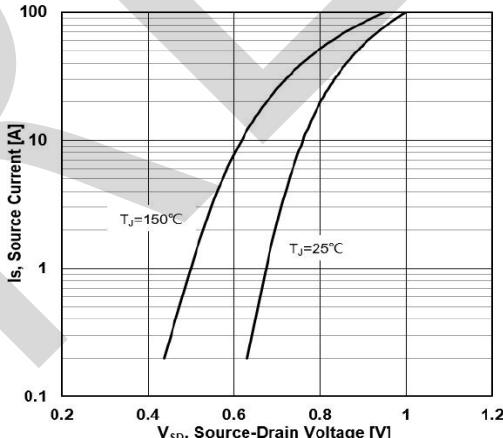


Figure 5. $R_{DS(ON)}$ vs. Junction Temperature

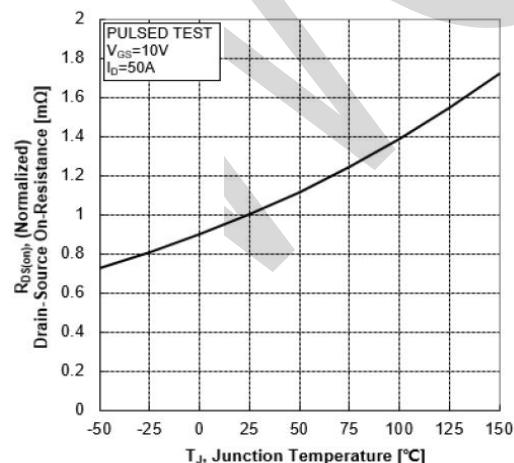
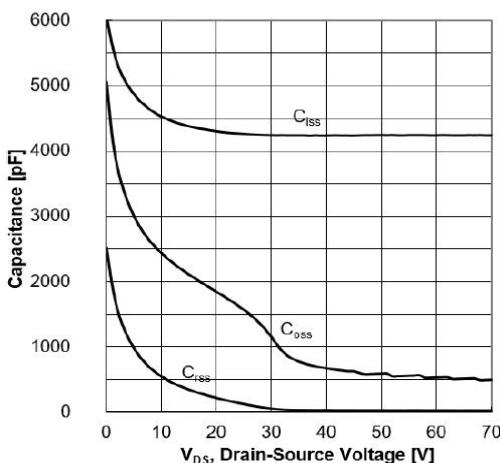


Figure 6. Capacitance Characteristics



Typical Characteristics

Figure 7. Gate Charge

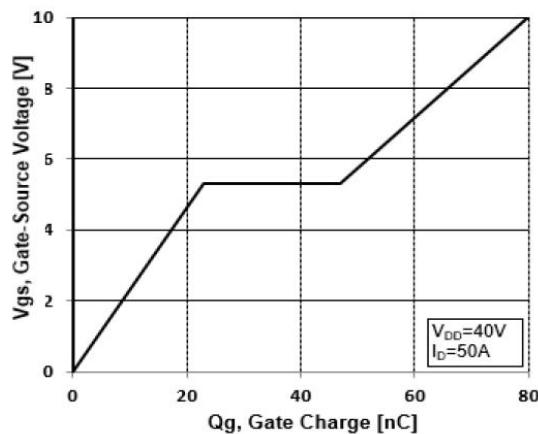


Figure 8. Maximum Power Dissipation vs Case Temperature

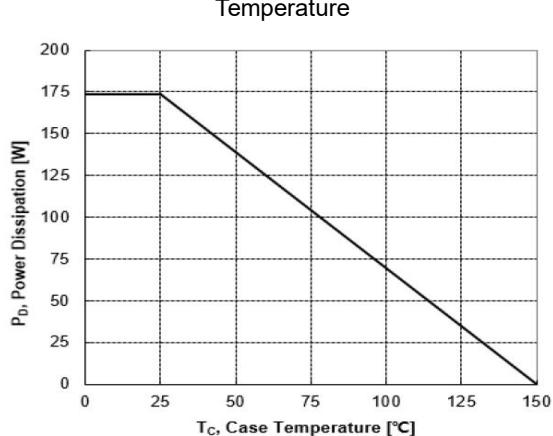


Figure 9. Maximum Safe Operating Area

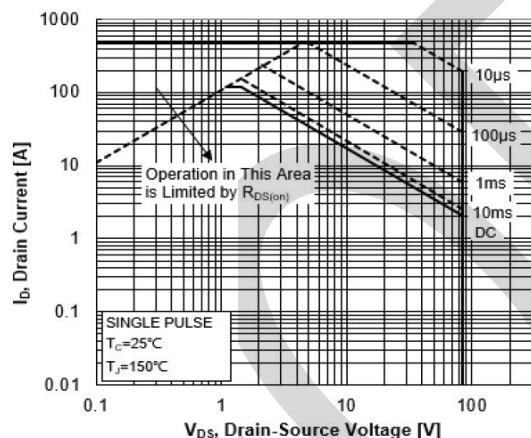


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

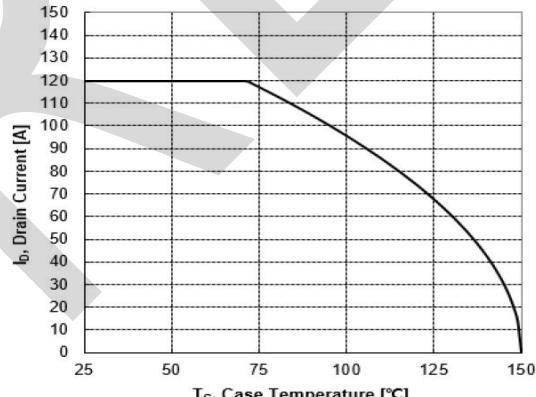
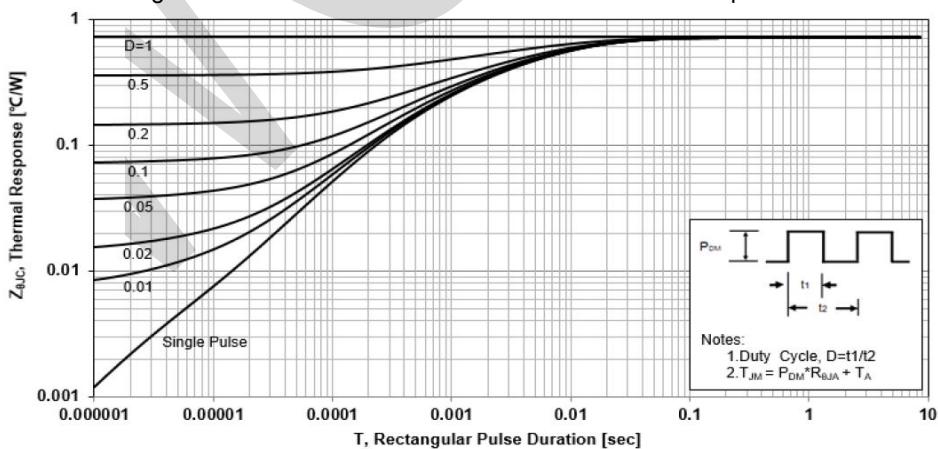
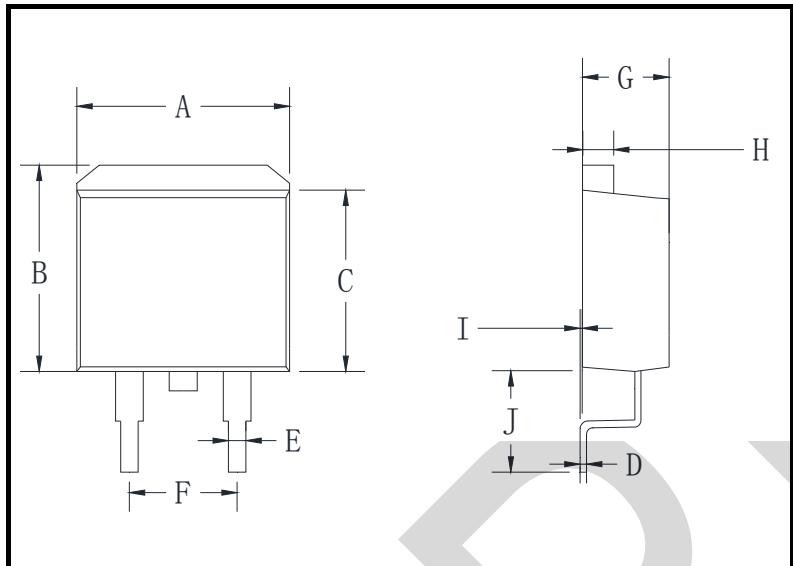


Figure 11: Normalized Maximum Transient Thermal Impedance



PACKAGE OUTLINE DIMENSIONS

TO-263



TO-263 mechanical data

UNIT		A	B	C	D	E	F	G	H	I	J
mm	max	11.5	10.5	9.0	0.64	0.94	5.6	5.1	1.4	0.6	6.1
	min	9.5	9.7	8.4	0.28	0.68	4.5	4.0	1.1	0	4.9
mil	max	452.7	413.3	354.3	25.2	37.0	220.5	200.8	55.1	23.6	240.1
	min	374.0	381.8	330.7	11.0	26.7	177.2	157.5	43.3	0	192.9

TO-263 Suggested Pad Layout

